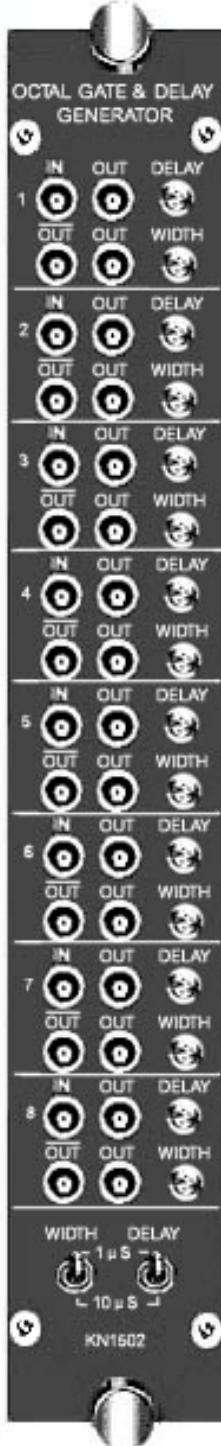


OCTAL GATE and DELAY GENERATOR

«ABSTRACT»



«Specifications»

Input characteristics

- ◆ Number of inputs : 1 × 8ch
- ◆ Impedance : 50Ω / NIM signal
- ◆ Minimum pulse width Approx.10ns

Output characteristics :

- ◆ OUTPUT 2x8ch & Invert output 1x8ch
- ◆ Impedance : 50Ω / NIM signal
- ◆ Delay time
 - switch 1 μS (38nS~1.3 μS variable continuously)
 - 10 μS (38nS~1.3 μS variable continuously)
- Pulse width
 - switch 1 μS (18nS~1.3 μS variable continuously)
 - 10 μS (0.1nS~13 μS variable continuously)

◆ Input and output connector : Standard LEMO connector.

◆ Packaging : NIM standard single-width module.

◆ Consumption current -6V : Approx.2.5A
+6V: Approx.200mA
-24V: Approx.200mA



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