CAMAC Module

Model KC5171

DUAL PEAKHOLD ADC & MEMORY

≪ABSTRACT≫

◆This module is a peak-hold type A/D converter. As the A/D converter is done by external gate signal,it can converts the signal that don't need.

◆As this module has 64k x 12bit memory in each channel, it can use as a multi-channel analyzer.

≪Specifications≫

◇Input characteristics ;

Number of channels : 2
Polarity : positive
Level : +5V max.
Risetime : 30nS min.
GATE : 2／NIM signal

◇ADC／MEMORY

Resolution : 12bit
Memory : 64k×12bit／ch

CAMAC function code is as follows.

◇F(0)A(n) : Read Channel Data & Increment RAM address counter.
◇F(1)A(n) : Read Channel RAM address counter.
◇F(8)A(n) : Test Channel LAM.
◇F(9)A(0) : Clear (Address counter).
◇F(10)A(n) : Clear Channel LAM.
◇F(16)A(n) : Write Channel Data & Increment RAM address counter.
◇F(17)A(n) : Set Channel RAM Address Counter.
◇F(24)A(0) : Disable LAM.
◇F(25)A(n) : Channel Gate Inhibit (Disable).
◇F(26)A(0) : Enable LAM.
◇F(27)A(n) : Channel GateEnable (ADC Start).
◇*1n=0.1 *2 LAM is address.
It is set when the counter overflows.

◆The input and output connector : Standard LEMO connector.

◆Packaging : CAMAC standard single-width module.

KAIZUWORKS CORPORATION
9-18 HIGASHIMATSUBARA HAKONEGASAKI MIZUHOMACHI
NISHITAMAGUN TOKYO 190-1222 JAPAN
Tel : 042-568-0866 Fax : 042-568-0867
E-mail kaizuwks@kaizuworks.co.jp Homepage http://www.kaizuworks.co.jp